

Electronic Acknowledgement Receipt

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Title of Invention:	Insulated-gate semiconductor device and approach involving junction-induced intermediate region
First Named Inventor/Applicant Name:	Kailash Gopalakrishnan
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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Request for status of Application	STFD035USSTATQ.pdf	31786	no	1

Warnings:

Information:		
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